Fig. 1

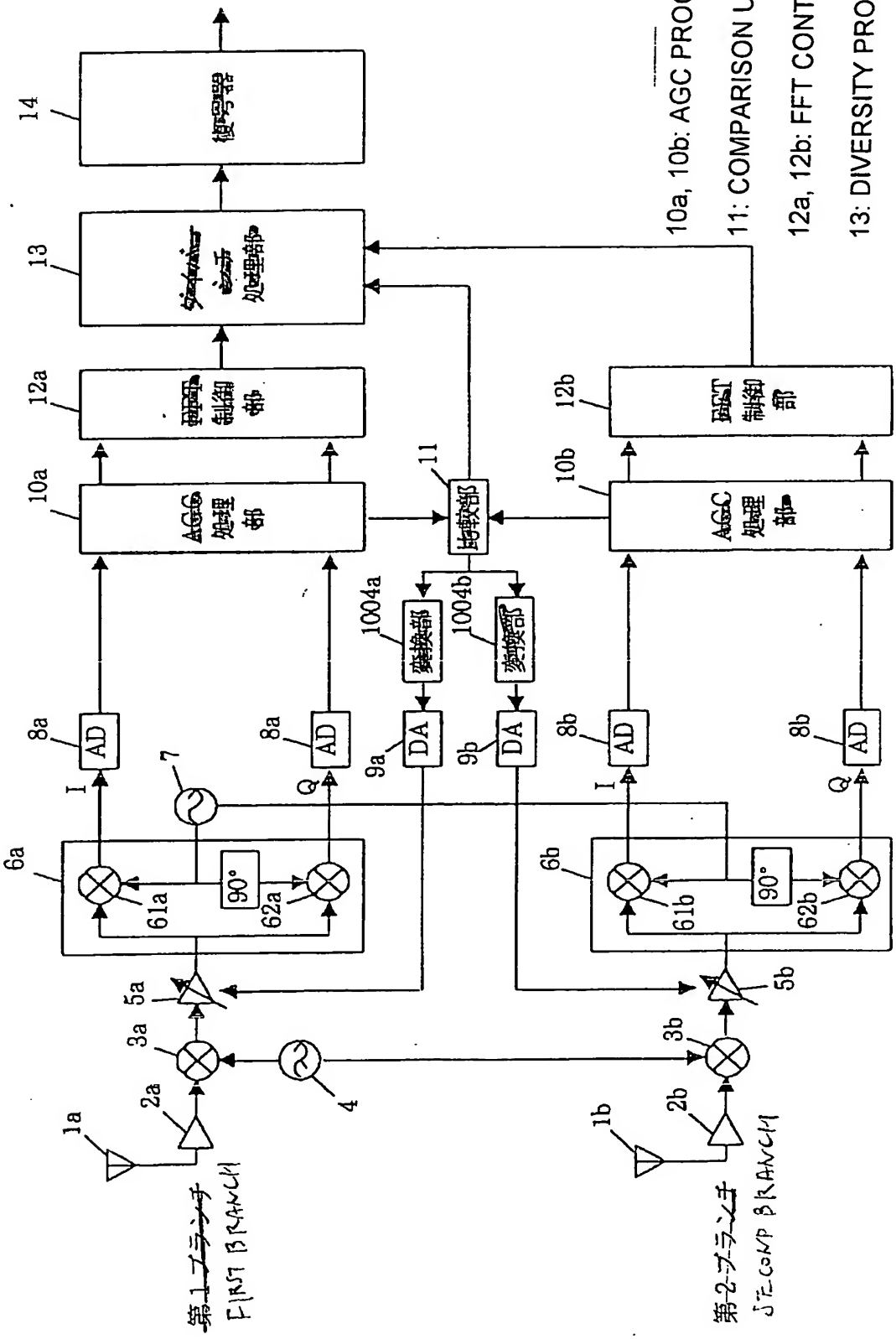
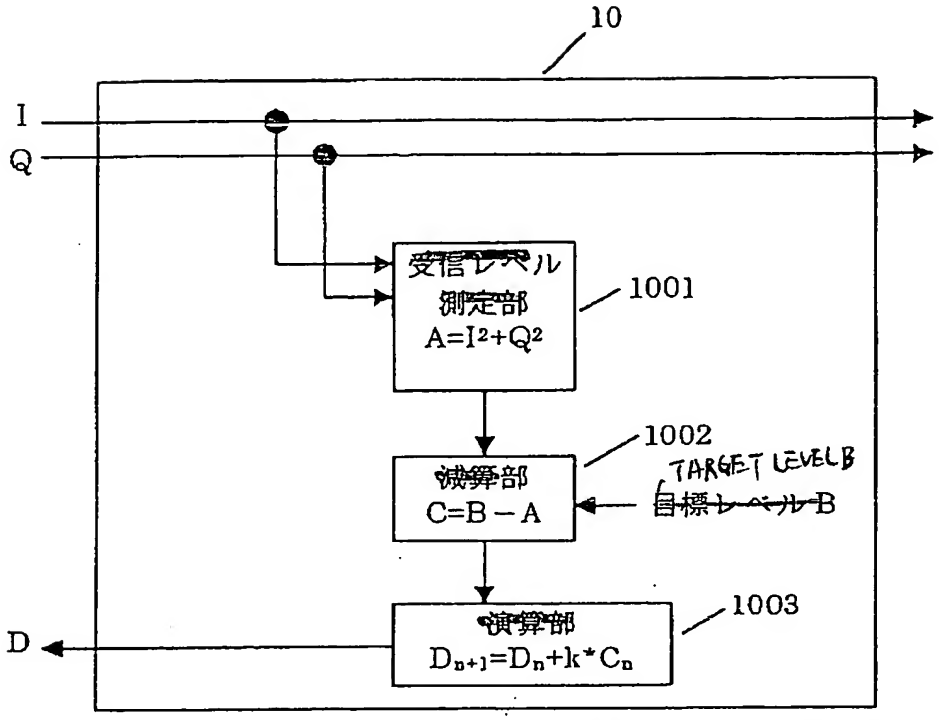

10a, 10b: AGC PROCESSING UNIT
11: COMPARISON UNIT
12a, 12b: FFT CONTROL UNIT
13: DIVERSITY PROCESSING UNIT
14: DECODER
1004a, 1004b: CONVERSION UNIT
AGC 処理部
比較部
FFT 制御部
ダイバーシティ 処理部
復号器
変換部
AD
DA
90°
I
Q
第1ブランチ FIRST BRANCH
第2ブランチ 2ND BRANCH

図2

Fig. 2



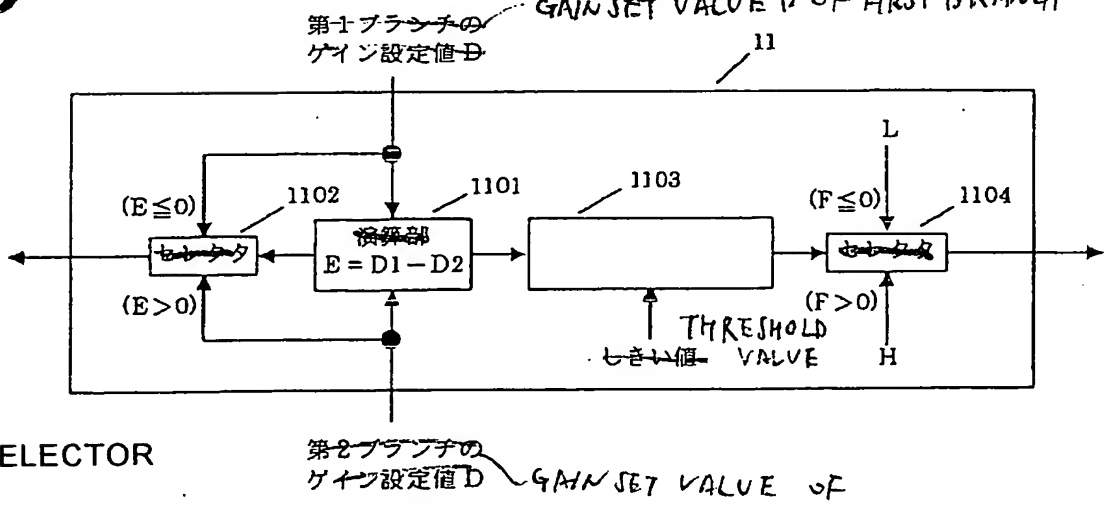
1001: RECEPTION LEVEL MEASURING UNIT

1002: SUBTRACTION UNIT

1003: ARITHMETIC OPERATION UNIT

図3

Fig. 3



1102: SELECTOR

1101: ARITHMETIC OPERATION UNIT

1104: SELECTOR

Fig. 4

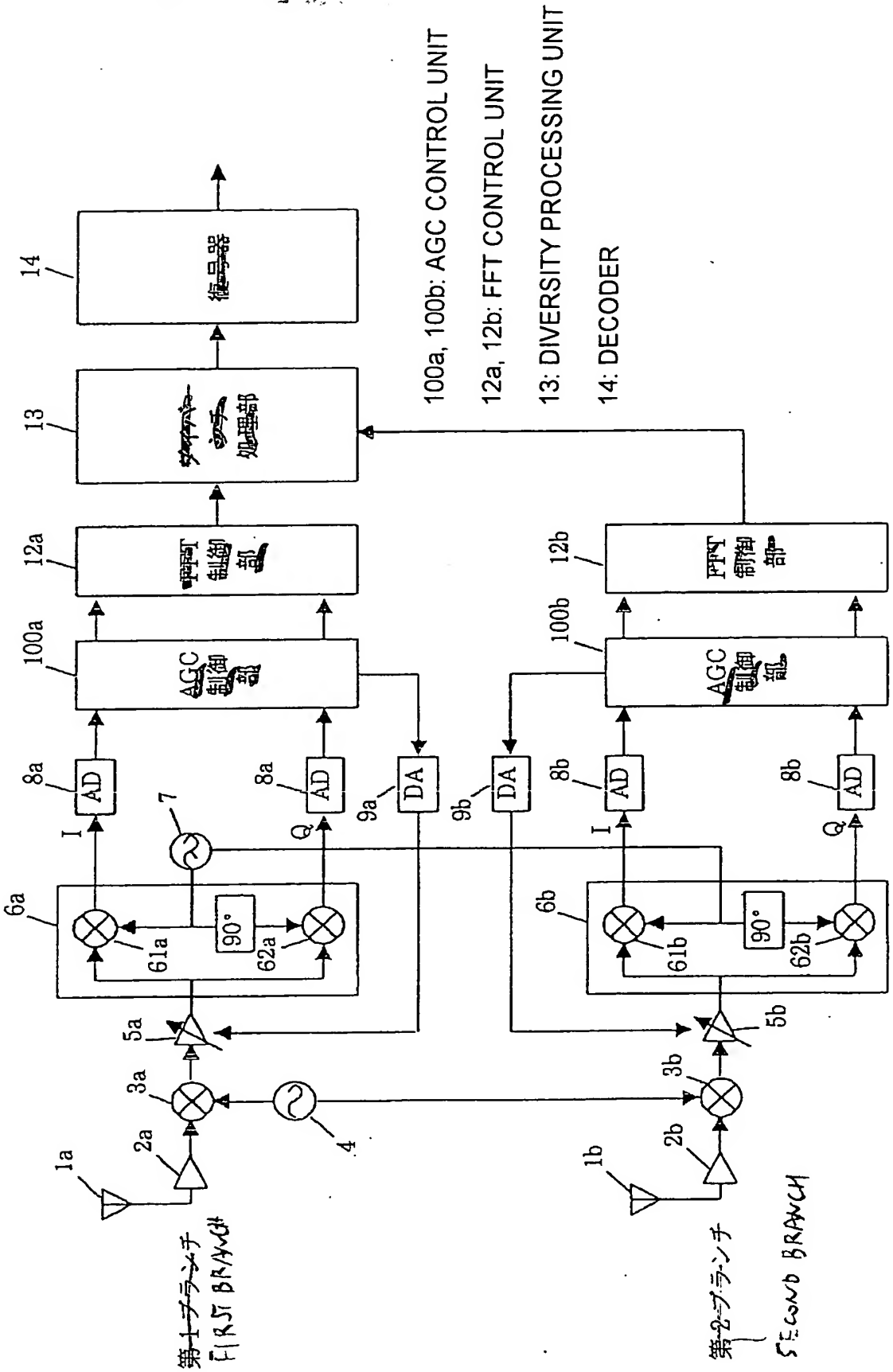

100a, 100b: AGC CONTROL UNIT
12a, 12b: FFT CONTROL UNIT
13: DIVERSITY PROCESSING UNIT
14: DECODER
14
復号器
13
ダイバーシティ処理部
12a
FFT制御部
12b
100a
AGC制御部
100b
8a
AD
8b
9a
DA
9b
I
Q
7
6a
6b
61a
61b
90°
62a
62b
5a
5b
3a
3b
4
1a
1b
2a
2b
第1ブランチ
FIRST BRANCH
第2ブランチ
SECOND BRANCH

Fig. 5

図5

(a) FIRST BRANCH

(b) SECOND BRANCH

(c) OUTPUT RESULT OF SELECTION DIVERSITY

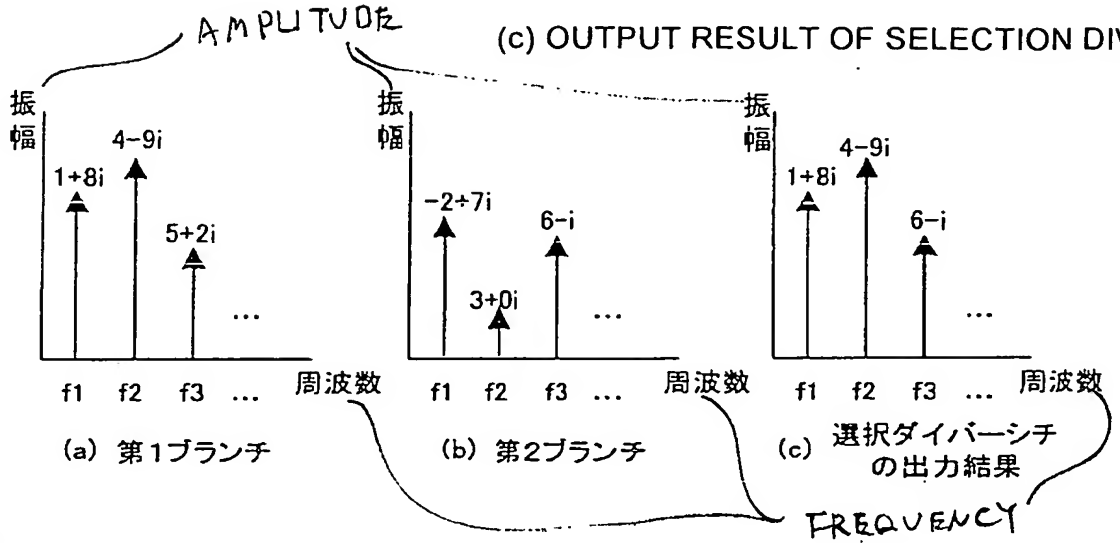
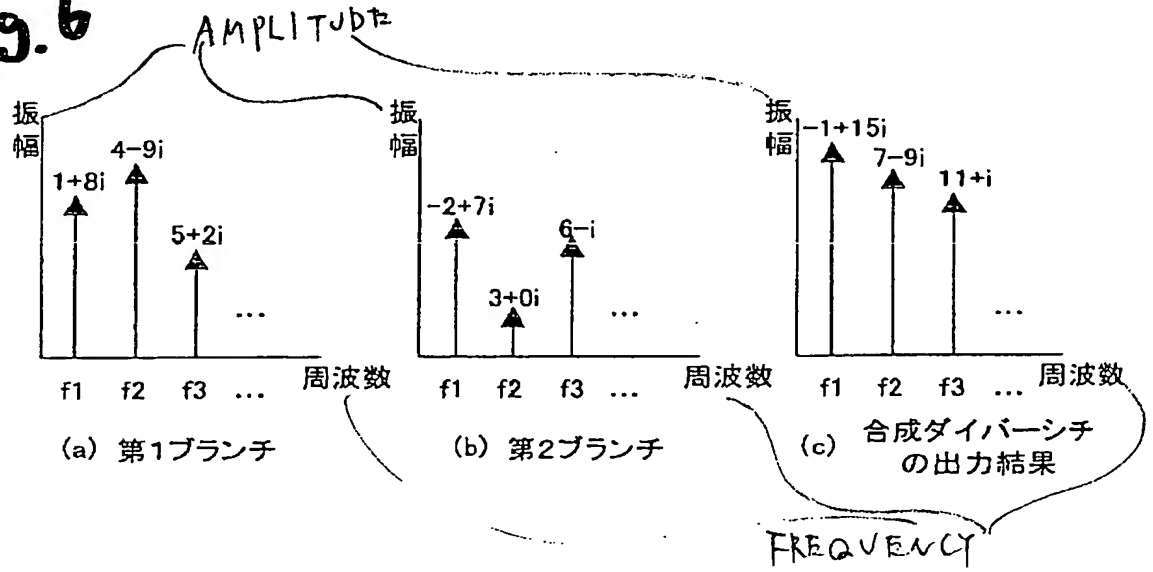


図6

Fig. 6



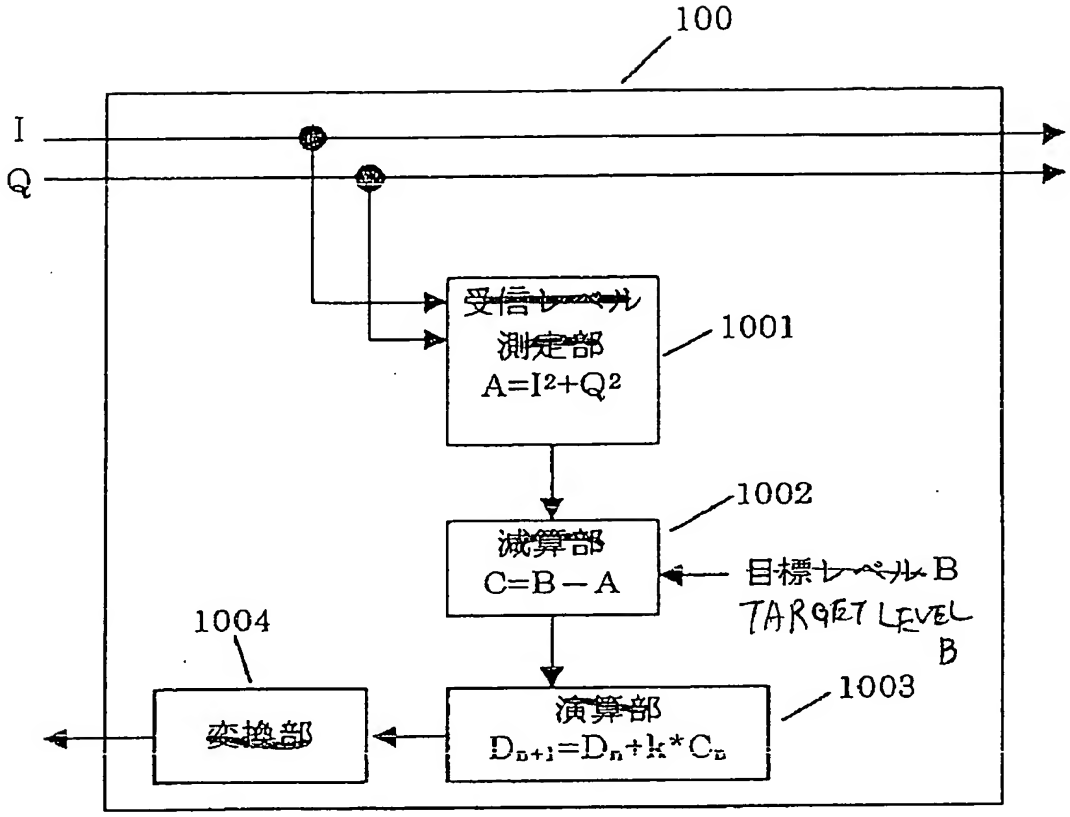
(a) FIRST BRANCH

(b) SECOND BRANCH

(c) OUTPUT RESULT OF COMBINING DIVERSITY

Fig.7

図7



1001: RECEPTION LEVEL MEASURING UNIT

1002: SUBTRACTION UNIT

1003: ARITHMETIC OPERATION UNIT

1004: CONVERSION UNIT